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10/008,918	12/03/2001	Neil Gibson	TI-32968	7227	
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Dan Swayze Texas Instruments Incorporated P.O. Box 655474			EXAMINER		
			NGUYEN, LINH V		
M/S 3999 Dallas, TX 75265			ART UNIT	PAPER NUMBER	
Danus, 17 752	203		2819		
			DATE MAILED: 09/23/2002	DATE MAILED: 09/23/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

Application No. Applicant(s)			Mc				
## Defice Action Summary ## Examiner Linh V Nguyen 219 ## Frend for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. **Extendent of line may be available, useder the provisions of 17 CPR 1.158(s). In on event, however, may a raply be linely filled ## If the period for raply is apecified above, the reasonable of line in the provisions of 17 CPR 1.158(s). In on event, however, may a raply be linely filled ## If the period for raply is apecified above, the reasonable of line in the line he period is less than this; (0) stay, a raply which the distulatory interiors of this; (26) stays will be considered timely. ## If the period for raply is apecified above, the reasonable value and the line which is less than the line motion after the mailing date of this communication, even if timely filled, may reduce any search papear term ediparent ediparent term ediparent term ediparent ediparent term ediparent term ediparent term ediparent ediparent ediparent ediparent term ediparent ediparent ediparent ediparent term ediparent term ediparent term ediparent edipare		Application No.	Applicant(s)				
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- The MAILING DATE of this communication appears on th cov r sh t with the correspond note address — Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. Extendince of them may be available under the provisions of 3 CFR 1.136(a). In no event, however, may a reply be timely filled If the period for reply specified above is less than thirty (39) days, a reply within the satutory minimum of thirty (30) days, will be considered timely. If the period for reply specified above is less than thirty (39) days, a reply within the satutory repload all page and oil capits (31) (MONTHS from the mailing date of the communication. Fallure is reply within the set or estanded privated for reply with the standard period will reply set. (MONTHS from the mailing date of the communication to secons ASANCONED (30 U.S. C. § 1.133). This action is FINAL. 2b) This action is non-final. 3) Responsive to communication (s) filed on 26 February 2002. 2a) This action is FINAL. 2b) This action is non-final. 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213. Disposition of Claims 4) Of the above claim(s) is a replication. 4a) Of the above claim(s) is a replication and/or election requirement. Application Papers 9) The specification is objected to by the Examiner. 10) The drawing(s) filed on 26 February 2002 [s/are: a) Cacepted or b) disapproved by the Examiner. Application Papers 9) The proposed drawing correction filed on is: a) approved by disapproved by the Examiner. 11) The proposed drawing correction filed on is: a) approved by disapproved by the Examiner. 12) The oath or declaration is objected to by the Examiner. 12) The oath or declaration is objected to by the Examiner. 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a) (d) or (f). 3) Acknowledgment is	Office Action Summary	Examiner	Art Unit				
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Oath/Declaration

It does not include signature of inventors: Mr. Neil Gibson, and Mr. Marco Corsi.

Double Patenting

- 1. Claims 1 22, of this application conflict with claims 1 16 of Application No. 10005463. 37 CFR 1.78(b) provides that when two or more applications filed by the same applicant contain conflicting claims, elimination of such claims from all but one application may be required in the absence of good and sufficient reason for their retention during pendency in more than one application. Applicant is required to either cancel the conflicting claims from all but one application or maintain a clear line of demarcation between the applications. See MPEP § 822.
- 2. Claims 1 22, provisionally rejected under 35 U.S.C. 101 as claiming the same invention as that of claims 1 -16 of Application No. 10005463. This is a <u>provisional</u> double patenting rejection since the conflicting claims have not in fact been patented.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 4. Claims 1 5, 7 9, and 20 22, are rejected under 35 U.S.C. 102(b) as being anticipated by Uscategui et al. U.S. patent No. 5,789,982.

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Regarding to claim 1 and 2, Fig. 2 Uscategui et al. disclose a low distortion feedback amplifier, the amplifier comprising: a pre-driver sub-stage and a final sub-stage, the pre-driver sub-stage having a plurality of transistors being biased by a plurality of current sources, the pre-driver sub-stage being adapted to accept a current signal (lin) from an input gm cell (Q1 – Q4 [element gm is inherent to small signal model of amplifier circuit]); the pre-driver stage being further adapted to provide biasing to a plurality of transistors in the final sub-stage; and the pre-driver sub-stage being coupled to the final sub-stage (Q13, Q14) so as to provided current gain from input to output of lout= Bn*Bp*lin (inherent from beta gain of each transistor); and localized feedback circuitry enclosed in the output stage operable to correct signal errors more rapidly than an overall amplifier feedback loop (Col. 1 lines 9 – 12);

- 5. Regarding to claim 3, the operational amplifier further comprising inherent RHPZ cancellation operable to extend bandwidth (As applicant stated, "RHPZ cancellation operable to extend bandwidth is inherent to the structure of claim 1 invention. Therefore RHPZ must be inherent to Uscategui et al as applied to claim 1 above, also.).
- 6. Regarding to claim 4, wherein the plurality of transistors in the final sub-stage comprises 4 transistors (Q13, Q14) arranged as a complementary pair of differential transistors enclosed in a localized feedback system.
- 7. Regarding to claim 5, the operational amplifier output stage recited in use in an integrated circuit (Abstract).
- 8. Regarding to claim 7, wherein the pre-driver sub-stage comprises two circuits, the first pre-driver sub-stage (top 26) circuit being adapted to condition a positive portion

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inputted signal for transfer to a first final sub-stage circuit (Q13) of the final sub-stage, and the second pre-driver sub-stage (bottom 26) circuit being adapted to condition a negative portion of an inputted signal for transfer to a second final sub-stage (Q14) circuit of the final sub stage;

- 9. Regarding to claim 8, wherein the first final sub-stage circuit and the second final sub-stage circuit are interconnected at an output terminal node (32) such that the conditioned and amplified positive portion the signal and the conditioned and amplified negative portion o t the signal are joined in phase with minimal crossover distortion the output signal having the form lout= Bn*Bp*lin (inherent from beta gain of each transistor).
- 10. Regarding to claim 9, 20 22, the amplifier claimed device and method are deemed to be made clearly inherent by the amplifier of Uscategui et al. as applied to claim 1 5, 7, and 8 above.

Claim Rejections - 35 USC § 103

- 11. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 12. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Uscategui et al., in view of Applicant Admitted Prior Art (AAPA).

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The amplifier of Uscategui et al. as applied to claims 1 – 5 above, disclose every

aspect of applicant's claimed invention except not explicitly disclose the amplifier is use

in a DSL driver. However, in DSL application, using amplifier is a well-known art and

conventional as AAPA has disclosed in the background of the invention. Therefore it

would have been obvious to one having ordinary skill in the art at the time the invention

was made to apply the amplifier of Uscategui et al in DSL application for the purpose of

reduction harmonic distortion in amplifier circuit (Uscategui Col. 1 line 23 –13).

Contact Information

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Linh Van Nguyen whose telephone number is (703)

305-1934. The examiner can normally be reached from 8:30 - 5:00 Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Mr. Michael Tokar can be reached at (703) 305-3493. The fax phone

numbers for the organization where this application or proceeding is assigned are (703)

308-7722 for regular communications and (703) 308-7722 for After Final

communications. Any inquiry of a general nature or relating to the status of this

application or proceeding should be directed to the receptionist whose telephone

number is (703) 308-0956.

LVN

September 18, 2002

Michael Tokar

Supervisory Patent Examiner

Mila J. Tokar

Technology Center 2800